

Interference Searched EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 refresh\$3 near5 rate) same (vary or chang\$3)).clm.	US-PGPUB	OR	OFF	2007/02/01 11:16
L2	4	((dynamic adj random adj access adj memory) or DRAM) same (refresh\$3 near5 rate) same (vary or chang\$3)).clm.	US-PGPUB	OR	OFF	2007/02/01 11:13
L3	20	((portion or section or segment\$1 or block\$1 or cell\$1 or bank\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same (refresh near5 rate)).clm.	US-PGPUB	OR	OFF	2007/02/01 11:15
L4	2	((portion or section or segment\$1 or block\$1 or cell\$1 or bank\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same ((different or second or another) near5 refresh near5 rate)).clm.	US-PGPUB	OR	OFF	2007/02/01 11:16
L5	8	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 refresh\$3 near5 rate)).clm.	US-PGPUB	OR	OFF	2007/02/01 11:17

EAST Search History

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L1	756	((dynamic adj random adj access adj memory) or DRAM) same (refresh near2 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:10
L2	875	((dynamic adj random adj access adj memory) or DRAM) same (refresh near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:11
L3	844	((portion or section or segment\$1 or block\$1 or cell\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same (refresh near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:39
L4	774	(711/105).CCLS.	USPAT; USOCR	OR	OFF	2007/02/01 09:12
L5	273	(711/106).CCLS.	USPAT; USOCR	OR	OFF	2007/02/01 09:12
L6	13	3 and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:13
L7	17	3 and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:35
L8	46	((portion or part or section or segment\$1 or block\$1 or cell\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same ((refresh near5 rate) near10 (chang\$3 or vary))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:40

EAST Search History

L9	40	((portion or part or section or segment\$1 or block\$1 or cell\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same ((refresh near5 rate) near5 (chang\$3 or vary))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:41
L10	41	((portion or part or section or segment\$1 or block\$1 or cell\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same ((refresh\$3 near5 rate) near5 (chang\$3 or vary))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 09:51
L11	17	((portion or part or section or segment\$1 or block\$1 or cell\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same (different near5 refresh\$3 near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:09
L12	1631	(365/222).CCLS.	USPAT; USOCR	OR	OFF	2007/02/01 10:06
L13	110	3 and 12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:06
L14	14	8 and 12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:06
L15	17	((portion or part or section or segment\$1 or block\$1 or cell\$1 or bank\$1) near10 (dynamic adj random adj access adj memory) or DRAM) same (different near5 refresh\$3 near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:10
L16	0	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 different near5 refresh\$3 near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:10

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L17	193	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 refresh\$3 near5 rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:11
L18	37	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 refresh\$3 near5 rate) same (vary or chang\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:12
L19	39	((dynamic adj random adj access adj memory) or DRAM) same ((information or data) near10 refresh\$3 near5 rate) same (vary or variable or chang\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:12
L20	9	19 and @pd<="20030905"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/01 10:12

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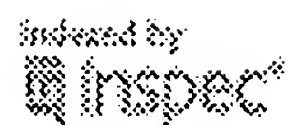
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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Multimedia LSI accelerator with embedded DRAM**
Sase, I.; Shimizu, N.; Yoshikawa, T.;
[Micro, IEEE](#)
Volume 17, Issue 6, Nov.-Dec. 1997 Page(s):49 - 54
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- ☐ 2. **The Implementation of a multi-view autostereoscopic display**
Moore, J.R.; Travis, A.R.L.; Lang, S.R.; Castle, O.M.;
[Stereoscopic Television, IEE Colloquium on](#)
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[AbstractPlus](#) | [Full Text: PDF\(708 KB\)](#) IEE CNF
- ☐ 3. **Bulk damage caused by single protons in SDRAMs**
Shindou, H.; Kuboyama, S.; Ikeda, N.; Hirao, T.; Matsuda, S.;
[Nuclear Science, IEEE Transactions on](#)
Volume 50, Issue 6, Part 1, Dec. 2003 Page(s):1839 - 1845
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[AbstractPlus](#) | [References](#) | [Full Text: PDF\(303 KB\)](#) IEEE JNL
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- ☐ 4. **Analysis of single-ion multiple-bit upset in high-density DRAMs**
Makihara, A.; Shindou, H.; Nemoto, N.; Kuboyama, S.; Matsuda, S.; Oshima, T.
H.; Buchner, S.; Campbell, A.B.;
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- ☐ 5. **Clinical utilization of grayscale workstations**
Stewart, B.K.; Aberle, D.R.; Boechar, M.I.; Barbaric, Z.; Taira, R.K.; Sayre, J.W.
[Engineering in Medicine and Biology Magazine, IEEE](#)
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Digital Object Identifier 10.1109/51.195945
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- ☐ **6. A case for intelligent RAM**
Patterson, D.; Anderson, T.; Cardwell, N.; Fromm, R.; Keeton, K.; Kozyrakis, C
Yelick, K.;
[Micro. IEEE](#)
Volume 17, Issue 2, March-April 1997 Page(s):34 - 44
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- ☐ **7. Fault-tolerant refresh power reduction of DRAMs for quasi-nonvolatile da**
Katayama, Y.; Stuckey, E.J.; Morioka, S.; Wu, Z.;
[Defect and Fault Tolerance in VLSI Systems. 1999. DFT '99. International Sym](#)
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Digital Object Identifier 10.1109/DFTVS.1999.802898
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- ☐ **8. Magnetoresistive random access memory using magnetic tunnel junction**
Tehrani, S.; Slaughter, J.M.; Deherrera, M.; Engel, B.N.; Rizzo, N.D.; Salter, J.
Dave, R.W.; Janesky, J.; Butcher, B.; Smith, K.; Grynkewich, G.;
[Proceedings of the IEEE](#)
Volume 91, Issue 5, May 2003 Page(s):703 - 714
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- ☐ **9. Tunneling-based SRAM**
van der Wagt, J.P.A.;
[Proceedings of the IEEE](#)
Volume 87, Issue 4, April 1999 Page(s):571 - 595
Digital Object Identifier 10.1109/5.752516
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- ☐ **10. The Titan Graphics Supercomputer architecture**
Diede, T.; Hagenmaier, C.F.; Miranker, G.S.; Rubinstein, J.J.; Worley, W.S., Jr
[Computer](#)
Volume 21, Issue 9, Sept. 1988 Page(s):13 - 30
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- ☐ **11. One-transistor GaAs MESFET- and JFET-accessed dynamic RAM cells fo**
medium density applications
Dungan, T.E.; Neudeck, P.G.; Melloch, M.R.; Cooper, J.A., Jr.;
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Digital Object Identifier 10.1109/16.55743
[AbstractPlus](#) | Full Text: [PDF\(824 KB\)](#) [IEEE JNL](#)
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- ☐ **12. 50th Annual Device Research Conference**
[Electron Devices. IEEE Transactions on](#)
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Stellwag, T.B.; Cooper, J.A., Jr.; Melloch, M.R.;
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- ☐ **14. Single event upset test structures for digital CMOS application specific integrated circuits**
Baze, M.P.; Bartholet, W.G.; Braatz, J.C.; Dao, T.A.;
[Nuclear Science, IEEE Transactions on](#)
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- ☐ **15. Flash memory goes mainstream**
Dipert, B.; Hebert, L.;
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Volume 30, Issue 10, Oct. 1993 Page(s):48 - 52
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- ☐ **16. A low-cost graphics and multimedia workstation chip set**
Undy, S.; Bass, M.; Hollenbeck, D.; Kever, W.; Thayer, L.;
[Micro, IEEE](#)
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- ☐ **17. A fully asynchronous low-power error corrector for the DCC player**
Van Berkel, K.; Burgess, R.; Kessels, J.L.W.; Peeters, A.; Roncken, M.; Schali
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- ☐ **18. A 10 Mb frame buffer memory with Z-compare and A-blend units**
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- ☐ **19. A cost-effective RISC/DSP microprocessor for embedded systems**
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- ☐ **20. Radiation effect characterization and test methods of single-chip and mu 16 Mbit DRAMs**
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Volume 43, Issue 6, Part 1, Dec. 1996 Page(s):2974 - 2981
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- ☐ **21. Applying multimedia to medical imaging**
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- ☐ **23. Microcontroller design advantages for portable computing**
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- ☐ **24. The impact of Mpact 2**
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- ☐ **25. SOC's drive new product development**
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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Retention-aware placement in DRAM (RAPID): software methods for quas DRAM**
Venkatesan, R.K.; Herr, S.; Rotenberg, E.;
[High-Performance Computer Architecture, 2006. The Twelfth International Syn](#)
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- ☐ 2. **Clinical utilization of grayscale workstations**
Stewart, B.K.; Aberle, D.R.; Boechat, M.I.; Barbaric, Z.; Taira, R.K.; Sayre, J.W
[Engineering in Medicine and Biology Magazine, IEEE](#)
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- ☐ 3. **A case for intelligent RAM**
Patterson, D.; Anderson, T.; Cardwell, N.; Fromm, R.; Keeton, K.; Kozyrakis, C
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- ☐ 4. **Optimizing the DRAM refresh count for merged DRAM/logic LSIs**
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- ☐ 5. **Magnetoresistive random access memory using magnetic tunnel junction**
Tehrani, S.; Slaughter, J.M.; Deherrera, M.; Engel, B.N.; Rizzo, N.D.; Salter, J.
Dave, R.W.; Janesky, J.; Butcher, B.; Smith, K.; Grynkewich, G.;
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6. Tunneling-based SRAM

van der Wagt, J.P.A.;

[Proceedings of the IEEE](#)

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8. 50th Annual Device Research Conference

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Volume 39, Issue 11, Nov 1992

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9. Single event upset test structures for digital CMOS application specific ir

Baze, M.P.; Bartholet, W.G.; Braatz, J.C.; Dao, T.A.;

[Nuclear Science, IEEE Transactions on](#)

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10. Flash memory goes mainstream

Dipert, B.; Hebert, L.;

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Undy, S.; Bass, M.; Hollenbeck, D.; Kever, W.; Thayer, L.;

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Kim, D.; Young, J.; Milton, S.; Kim, H.J.; Kim, Y.;

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- ☐ **13. Radiation effect characterization and test methods of single-chip and mu 16 Mbit DRAMs**
LaBel, K.A.; Gates, M.M.; Moran, A.K.; Kim, H.S.; Seidleck, C.M.; Marshall, P. Carkhuff, B.;
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- ☐ **14. A simple method to determine the floating-body voltage of SOI CMOS de**
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- ☐ **15. Application of laser testing in study of SEE mechanisms in 16-Mbit DRAM**
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- ☐ **17. Bulk damage caused by single protons in SDRAMs**
Shindou, H.; Kuboyama, S.; Ikeda, N.; Hirao, T.; Matsuda, S.;
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